STATEMENT UNDER 37 CFR 3.73(b)	
Applicant/Patent Owner: SiGe Semiconductor, Inc.	
Application No./Patent No.: 10/588,728 Filed/Issue Date:	August 8, 2006
Titled: METHODS OF ENHANCING POWER AMPLIFIER LINEARITY	
SiGe Semiconductor, Inc. , a corporation	
(Name of Assignee) (Type of Assignee: corporation	on, partnership, university, government agency, etc.)
states that it is:	
1. the assignee of the entire right, title, and interest in;	
2. an assignee of less than the entire right, title, and interest in (The extent (by percentage) of its ownership interest is%); or	
3. the assignee of an undivided interest in the entirety of (a complete assignate was made)	gnment from one of the joint inventors
the patent application/patent identified above, by virtue of either:	
A. An assignment from the inventor(s) of the patent application/patent identifing in the United States Patent and Trademark Office at Reel 018172 copy thereof is attached. OR	
B. A chain of title from the inventor(s), of the patent application/patent identified	
1. From: To:	
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The document was recorded in the United States Patent and Trademark Office at	
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Additional documents in the chain of title are listed on a supplemental s	sheet (s).
As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.1	title from the original owner to the assignee
[NOTE: A separate copy (i.e., a true copy of the original assignment document(Division in accordance with 37 CFR Part 3, to record the assignment in the record	s)) must be submitted to Assignment ords of the USPTO. <u>See</u> MPEP 302.08]
The undersigned (whose title is supplied below) is authorized to act on behalf of	
Signature	Date 200 9
Gary S. Morris, Reg. No. 40,735	Legal Representative for the
Printed or Typed Name	Assignee Title